

HMI-200-8051-A

- Dual 4K TRACE Buffers
- Complex Event Triggering
- 128K Overlay Memory (256K Optional)
- Supports PSEN Line
- Supports Bank Switching
- 16 External Trace Input Leads
- 2 External Trigger Outputs
- Hardware Based Performance Analysis
- Source-Level Debugging Software

Full in-circuit emulation for the Intel MCS[®] 51 microcontroller family including the GA, GB, FA, FB, and FC versions. (Both standard and CMOS parts are supported.) External bank switching inputs allow the target hardware to select up to 4 banks of either target or emulator overlay memory. Sixteen external inputs can be used either as input conditions for triggering or trace inputs. Complex event and triggering sequences permit precise control of the trace thereby reducing the requirement for large trace buffers. Hardware based performance analysis is included at no additional cost and provides very accurate timing and code coverage information. Software development is enhanced by the use of SourceGate II, a full featured source-level debugging package that is available for Windows*, SUN or HP host platforms.

MICROCONTROLLERS
SUPPORTED:
8x3x, 8x5x, 8x5xFx, 8xC5xGx

DEVELOPMENT PLATFORMS:
PC (Non-Windows), PC Windows,
SUN Workstation, HP Workstation

AVAILABILITY:
Now

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